



JP21535.A05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshitaka SUMIDA

Group Art Unit: 2181

Appln. No. : 10/000,132

Examiner: A. ELAMIN

Filed : December 4, 2001

For : METHOD FOR REDUCING POWER CONSUMPTION OF CPU,  
ELECTRONIC APPARATUS, AND RECORDING MEDIUM HAVING  
POWER CONSUMPTION REDUCTION PROGRAM RECORDED  
HEREON

**COMPLETION OF RECORD**

Commissioner for Patents  
U.S. Patent and Trademark Office  
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401 Dulany Street  
Alexandria VA 22314

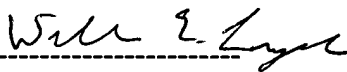
Sir:

On March 4, 2002, Applicants filed an Information Disclosure Statement in the above-captioned matter. In the Information Disclosure Statement, Japanese Patent Publication No. JP8-263326, which was discussed on page 3 of the specification of the present application, was brought to the Examiner's attention, a copy thereof was provided to the Examiner, and a Form PTO-1449 was also provided.

Also provided was an English language abstract of the Japanese Patent Publication, also cited on the Form PTO-1449. Although all of the patent documents were initialed, the English abstract translation was not initialed on the Form PTO-1449. To complete the record, Applicants assume that the English language abstract of the Japanese patent was considered by the Examiner during consideration of the underlying document.

Although no response to the above statements is deemed necessary, should the Examiner have any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,  
Yoshitaka SUMIDA

  
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February 25, 2005  
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